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**TRANSMITTAL  
FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

11

Application Number

10/765,387

Filing Date

January 26, 2004

First Named Inventor

Bicakci, Ara

Art Unit

2819

Examiner Name

Williams, Howard L.

Attorney Docket Number

03-1931

**ENCLOSURES (Check all that apply)**

Fee Transmittal Form



Fee Attached



Amendment/Reply



After Final



Affidavits/declaration(s)



Extension of Time Request



Express Abandonment Request



Information Disclosure Statement



Certified Copy of Priority Document(s)

Reply to Missing Parts/  
Incomplete ApplicationReply to Missing Parts  
under 37 CFR 1.52 or 1.53

Drawing(s)



Licensing-related Papers



Petition

Petition to Convert to a  
Provisional ApplicationPower of Attorney, Revocation  
Change of Correspondence Address

Terminal Disclaimer



Request for Refund



CD, Number of CD(s) \_\_\_\_\_

☐ Landscape Table on CD

After Allowance Communication to TC

Appeal Communication to Board  
of Appeals and InterferencesAppeal Communication to TC  
(Appeal Notice, Brief, Reply Brief)

Proprietary Information



Status Letter

Other Enclosure(s) (please identify  
below):

1. Return Postcard

Remarks

- Issue Fee Payment

**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm Name

LSI Logic Corporation

Signature

Printed name

Sandeep Jaggi

Date

2-15-04

Reg. No.

43,331

**CERTIFICATE OF TRANSMISSION/MAILING**

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature

Typed or printed name

Mark Salvatore

Date

2-17-05

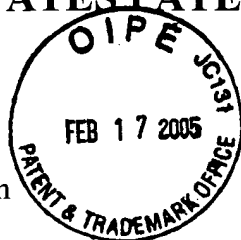
This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re Application of :**

Ara Bicakci  
Gurjinder Singh



**Serial No. :** 10/765,387

**Filed :** January 26, 2004

**For :** Method and Apparatus for  
Controlling Switch Noise in Digital-  
to-Analog Interface

**Group Art Unit :** 2819

**Examiner :** Williams, H.

**Atty Docket :** / 03-1931

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Mark Salvatore

February 17, 2005

Date

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## SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

**Official Draftsman**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milipitas, CA 95035  
408-433-7475

Date: 2-15-05

Respectfully submitted,

Sandeep Jaggi

Reg. No. 43,331